

ABSTRACT OF THE DISCLOSURE

An efficient broadband amplifier. The amplifier includes a mechanism for
5 amplifying an input signal via a high-speed switch and providing an amplified signal
in response thereto. Another mechanism filters the amplified signal via common
mode rejection and provides an output signal in response thereto. In a specific
embodiment, the mechanism for filtering includes a first mechanism for separating an
input signal into plural intermediate signals. The mechanism for amplifying includes
10 a second mechanism for amplifying the plural intermediate signals via one or more
high-speed switches and providing plural amplified signals in response thereto. The
mechanism for filtering further includes a third mechanism for employing common
mode rejection to filter the plural amplified signals, yielding a single output signal in
response thereto.